Serial Number: 10/607,772 Filing Date: June 27, 2003

Title: CACHE INTEGRITY APPARATUS, SYSTEMS, AND METHODS

Assignee: Intel Corporation

IN THE ABSTRACT OF THE DISCLOSURE

Please amend the Abstract as follows:

CACHE WRITE INTEGRITY LOGGING APPARATUS, SYSTEMS, AND METHODS

Abstract of the Disclosure

An apparatus, as well as systems, methods, and articles can operate to record the address of write operations to a memory cached by a non-volatile cache prior to executing an operating system cache driver. In an embodiment, a non-volatile cache may be implemented by creating a device option read only memory (ROM), or modifying the associated computer basic input-output system (BIOS) to trap software interrupts associated with disk and other media access requests. Associated addresses, such as logical block addresses, can be stored in a log for data that is modified. The resulting log can be stored in a non-volatile medium, including the cache itself. If the available log space is not large enough to record all write activity prior to loading operating system drivers, a flag may be set to indicate the overrun condition.